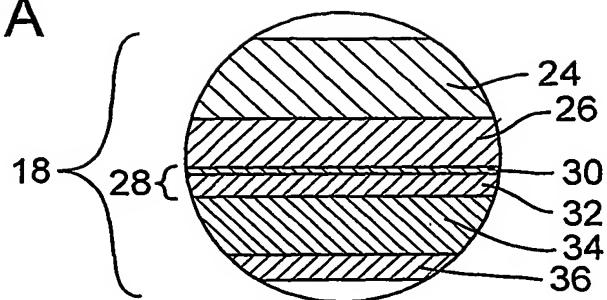
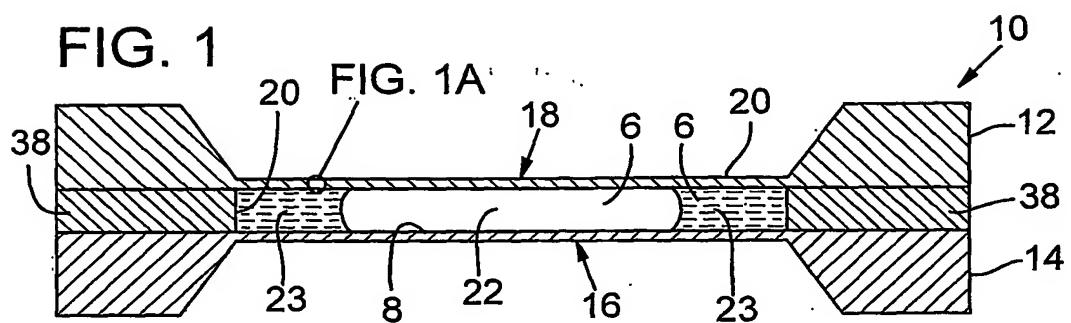
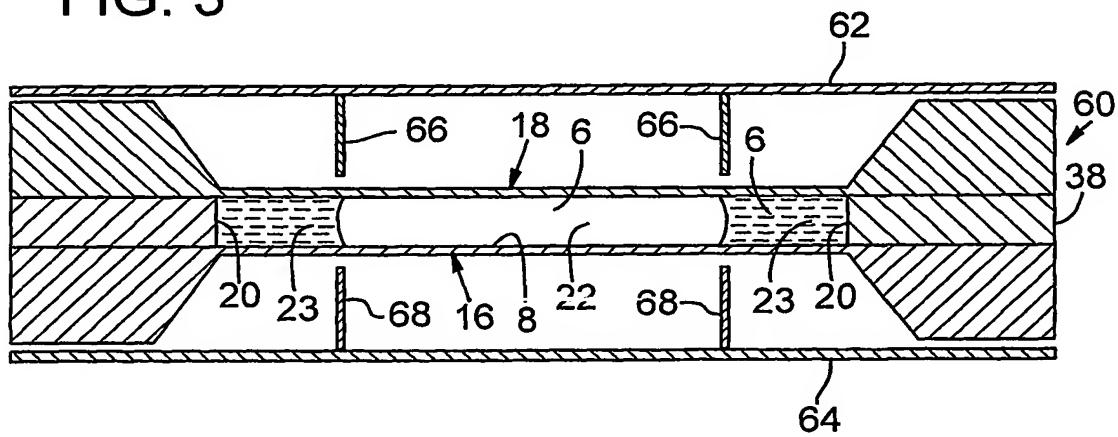


**FIG. 1A****FIG. 1****FIG. 3**

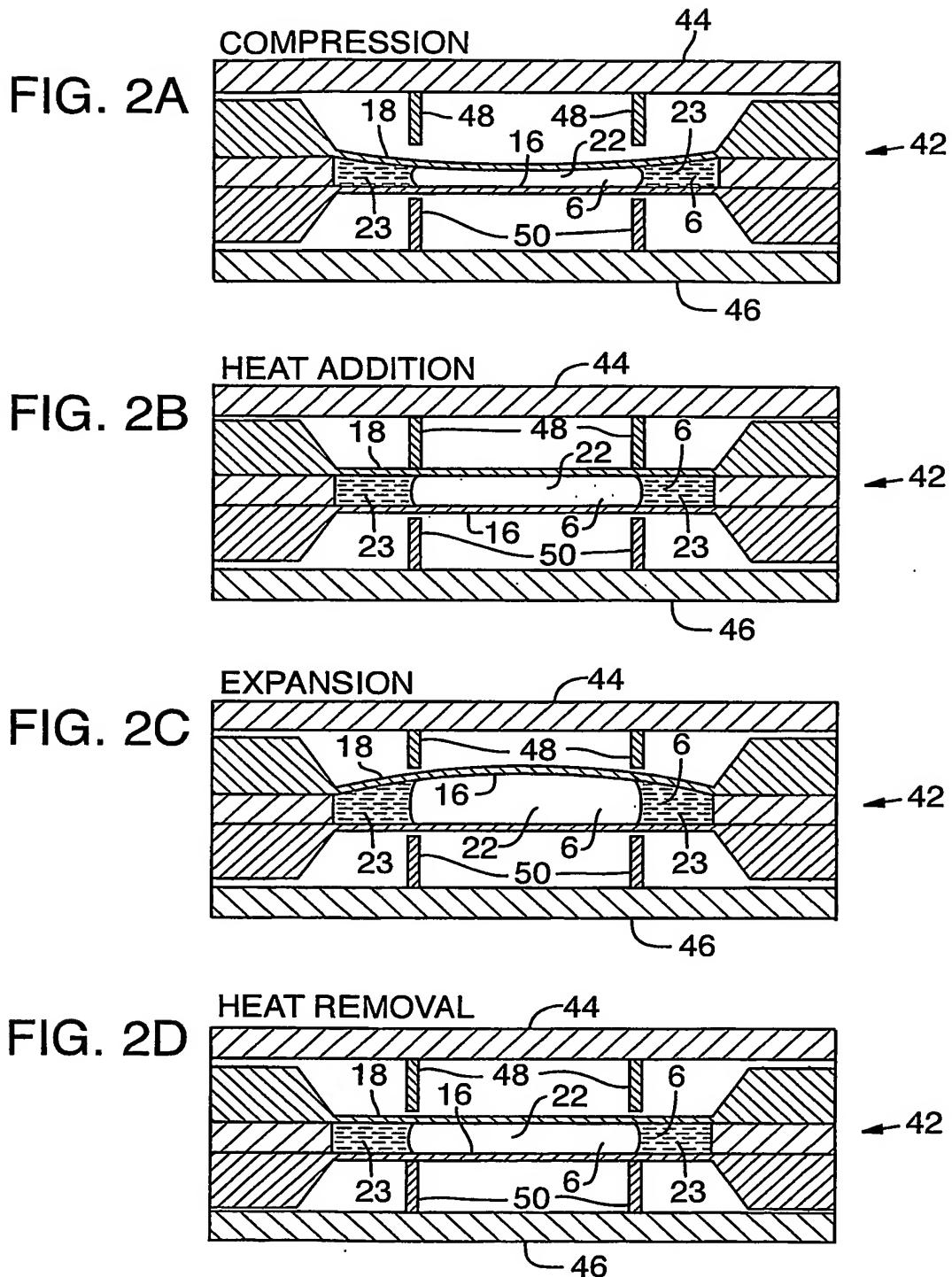


FIG. 4

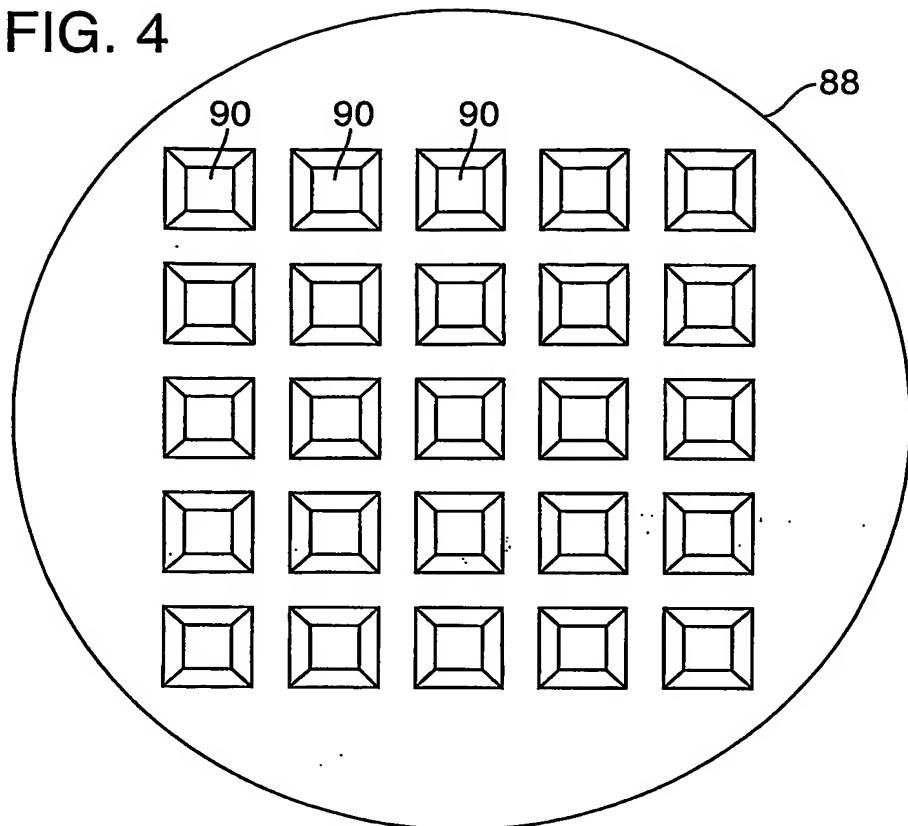
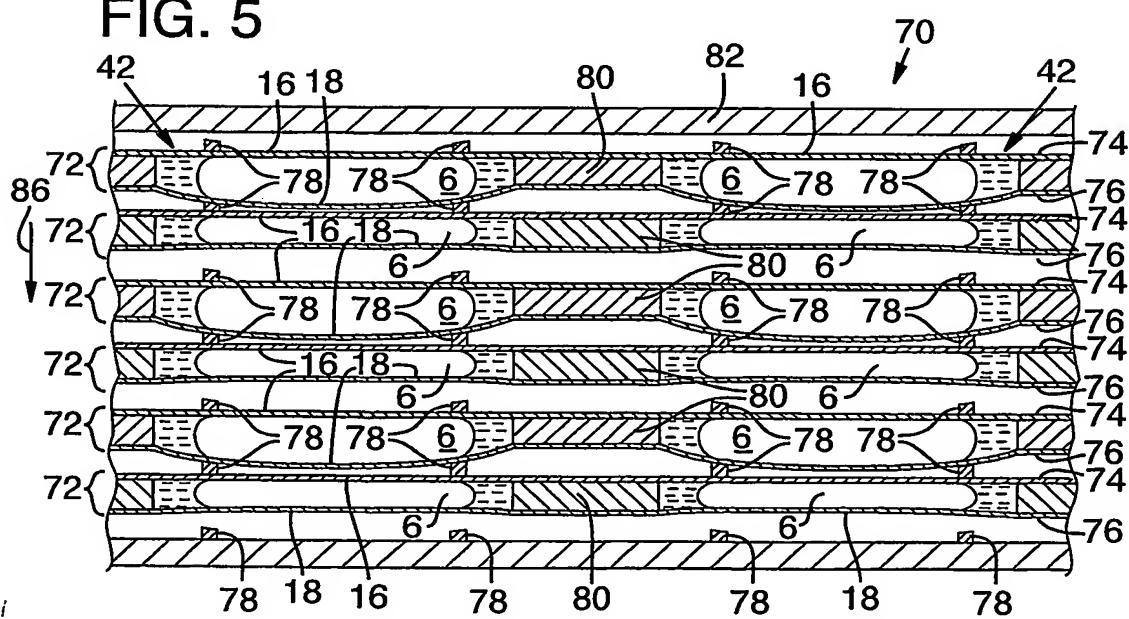


FIG. 5



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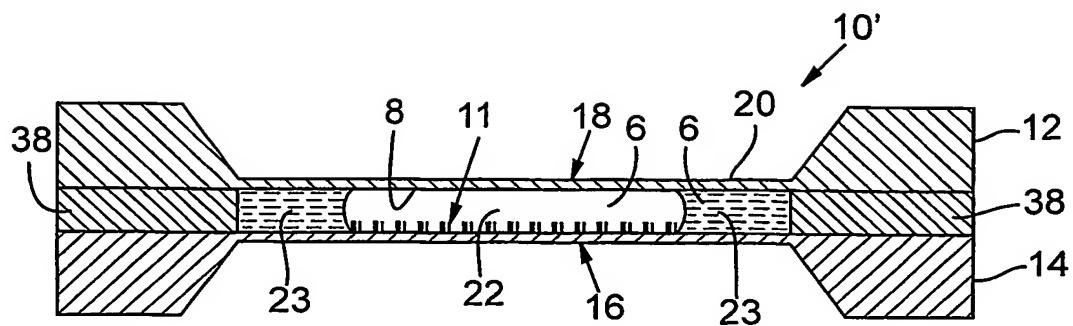


FIG. 6A

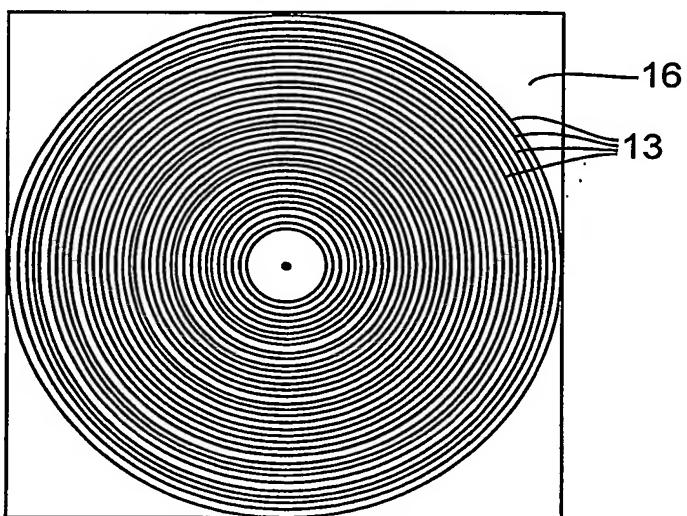


FIG. 6B

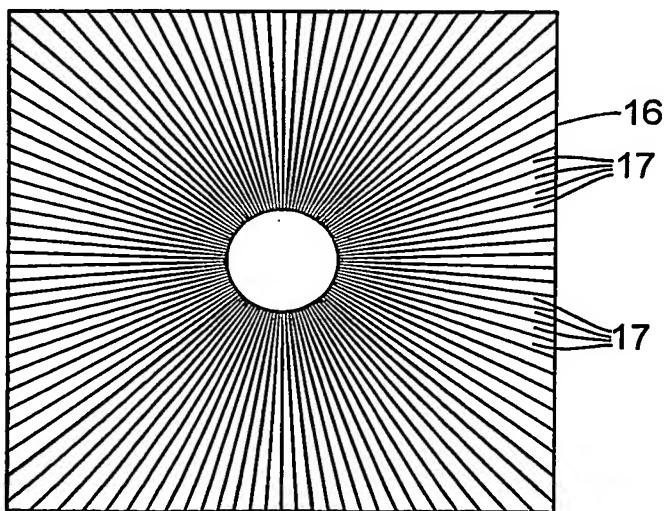
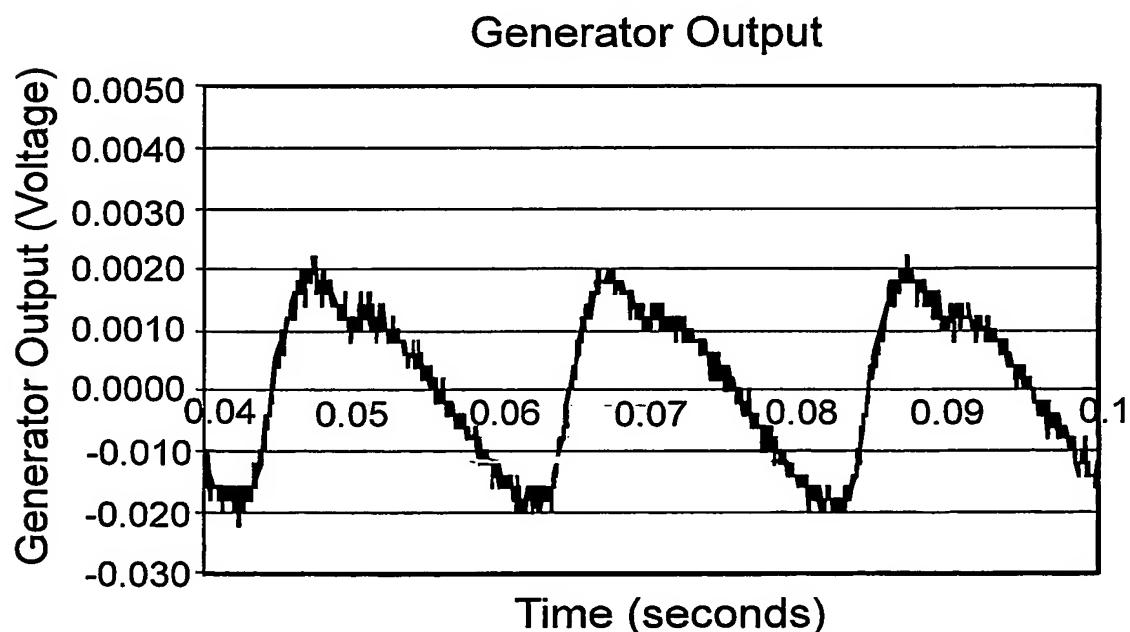
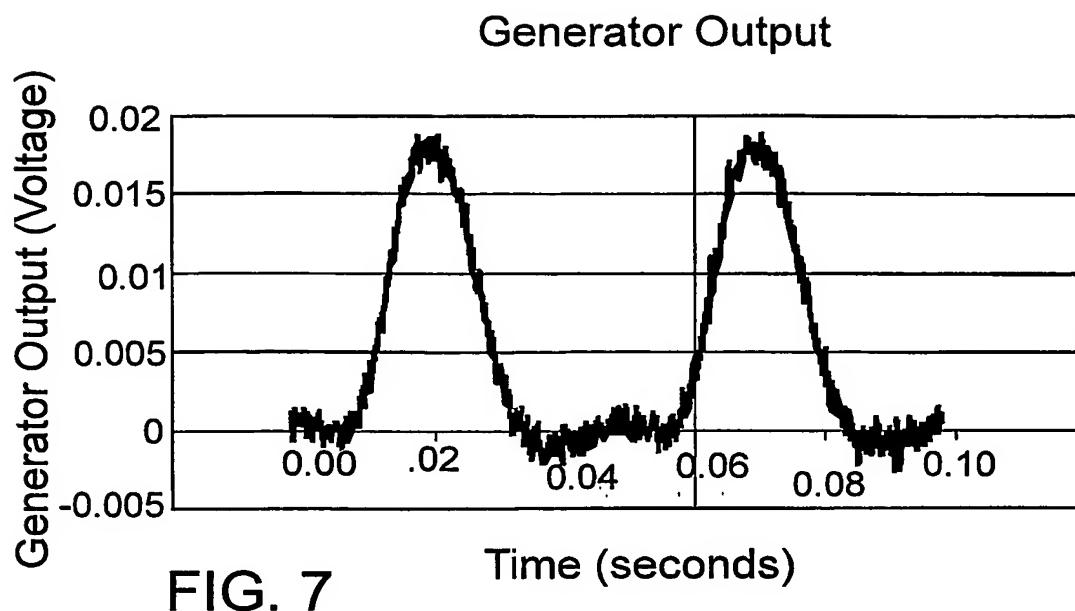


FIG. 6C



Thermal Resistance Measurements, Bare Silicon  
Thermal Switch, w/ 10 gram load applied

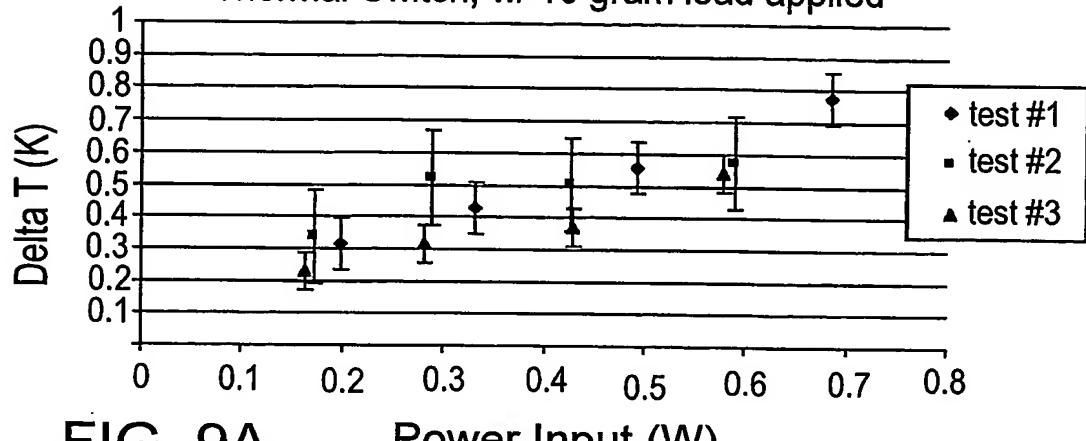


FIG. 9A

Power Input (W)

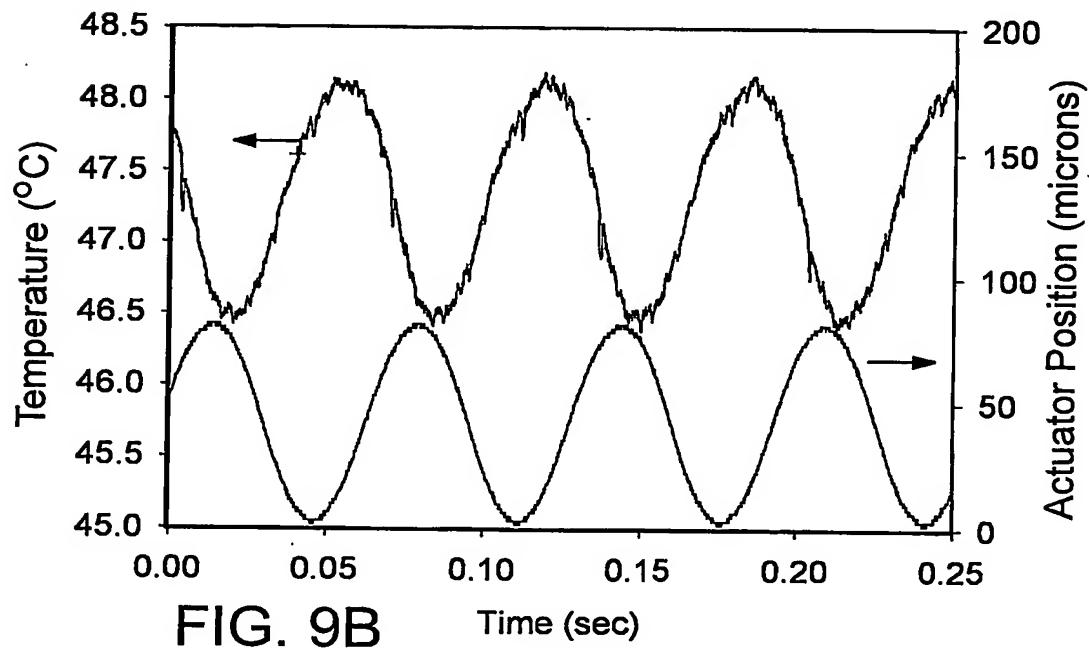


FIG. 9B

Time (sec)

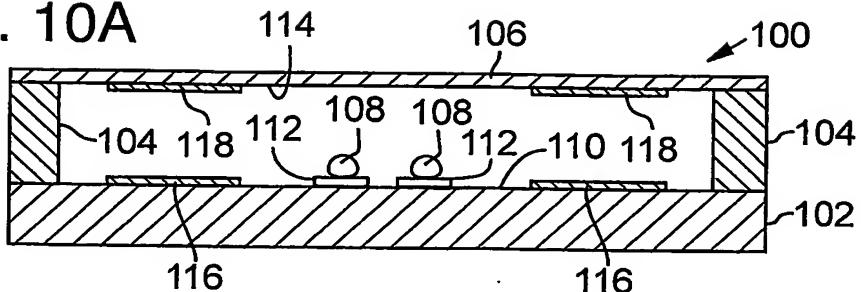
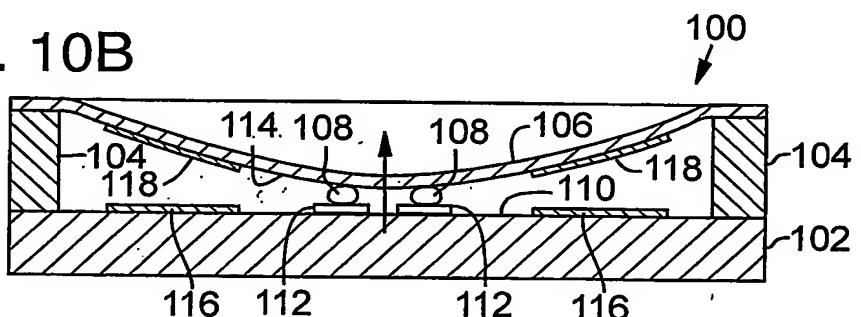
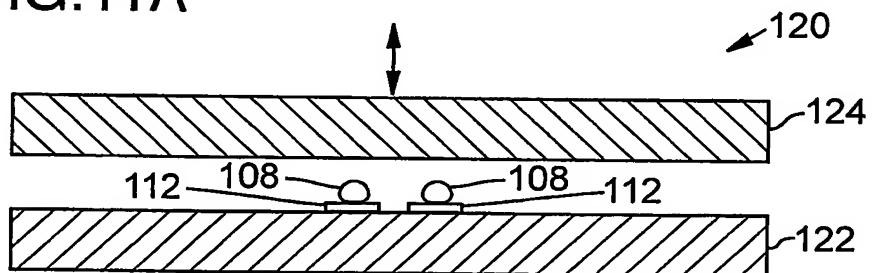
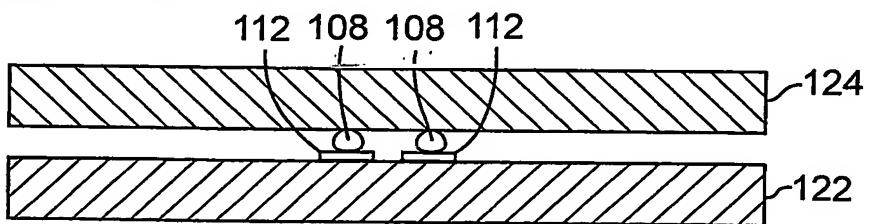
**FIG. 10A****FIG. 10B****FIG. 11A****FIG. 11B**

FIG. 12

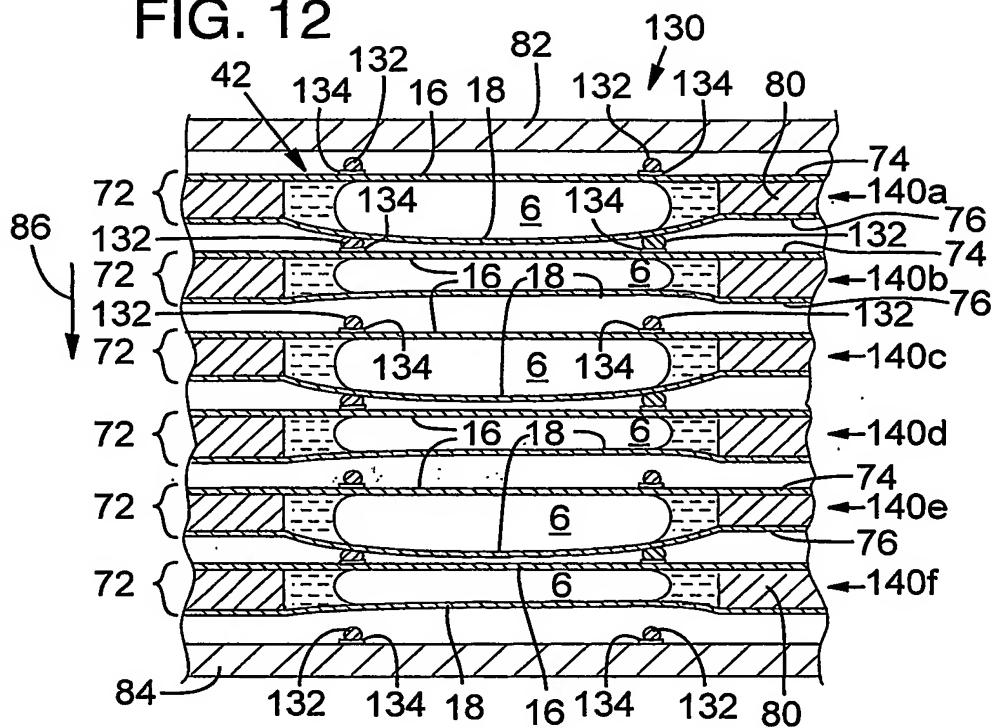
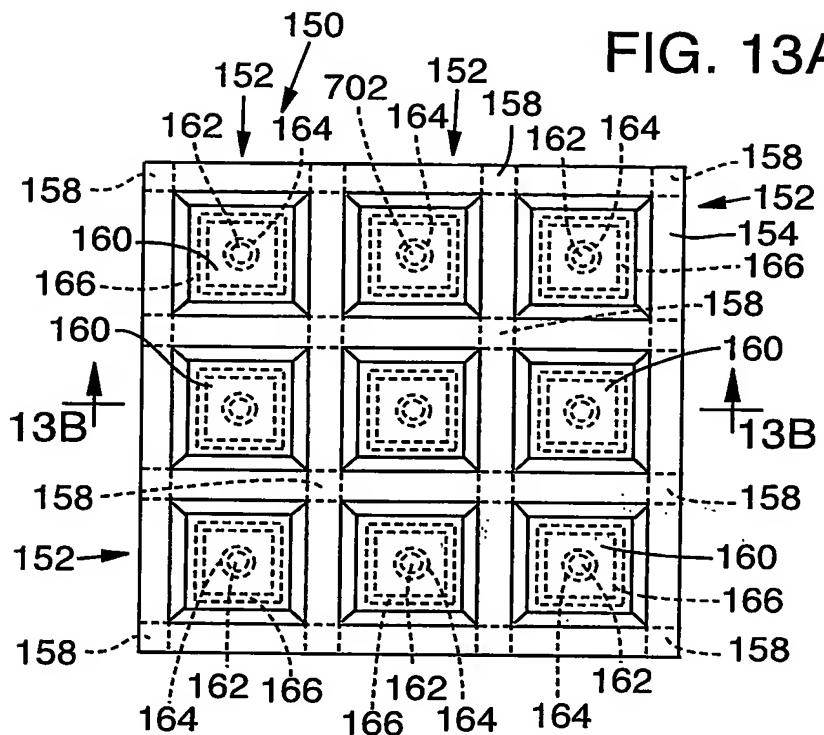
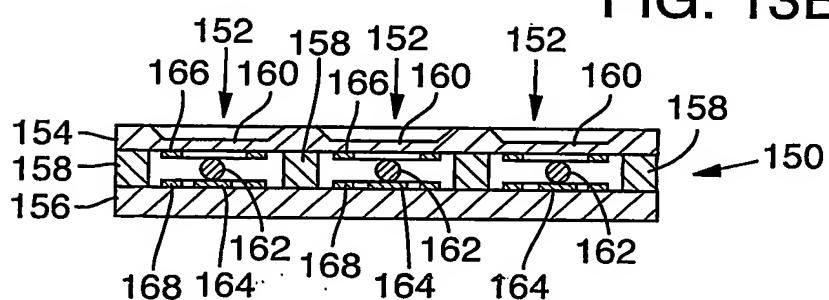


FIG. 13A



**FIG. 13B**



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FIG. 14

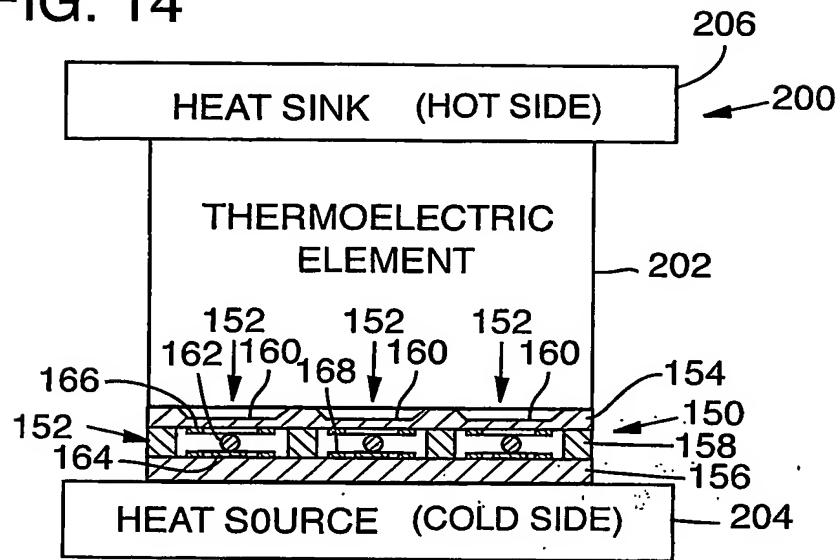
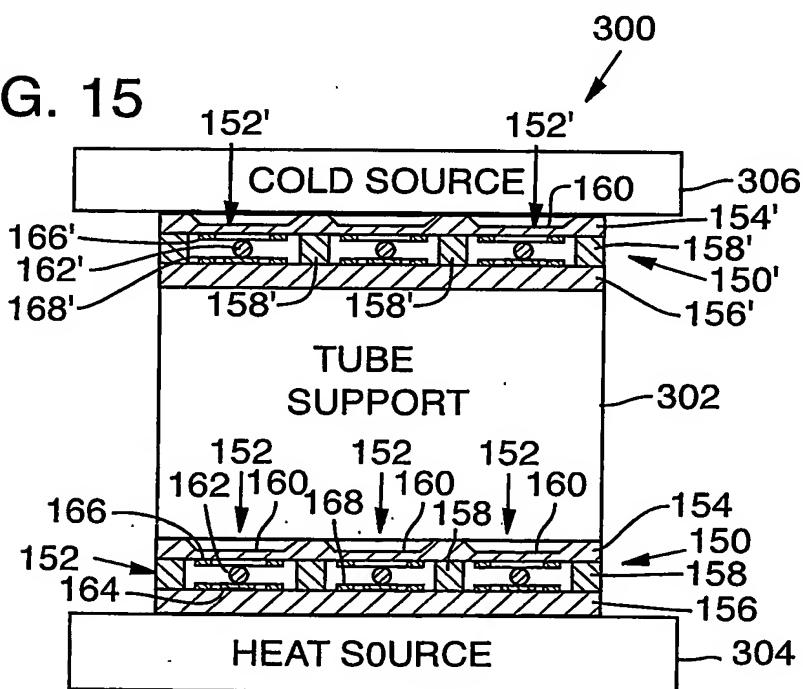


FIG. 15



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FIG. 16A

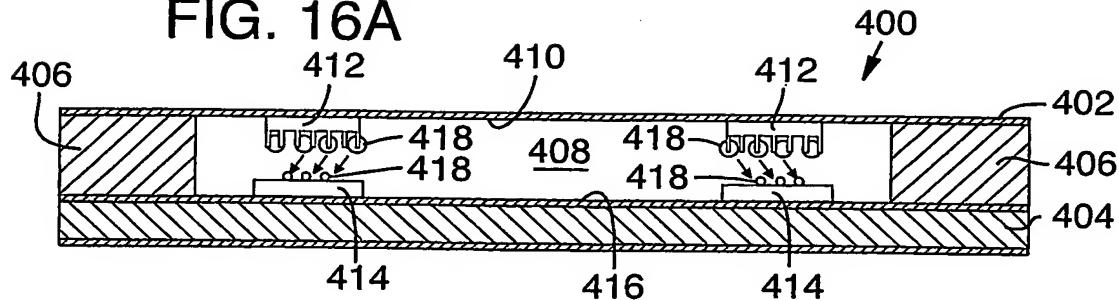


FIG. 16B

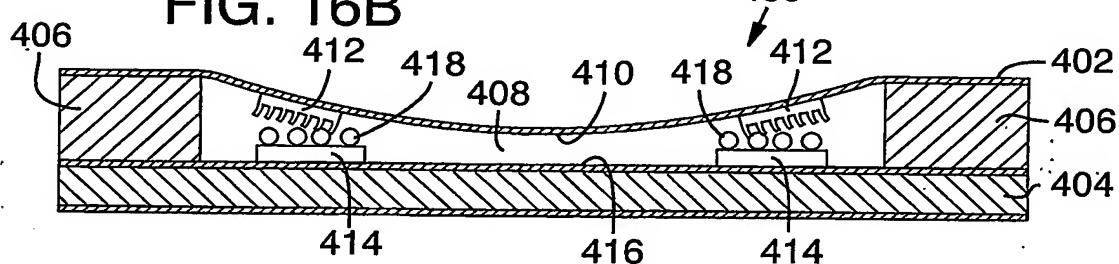


FIG. 17A

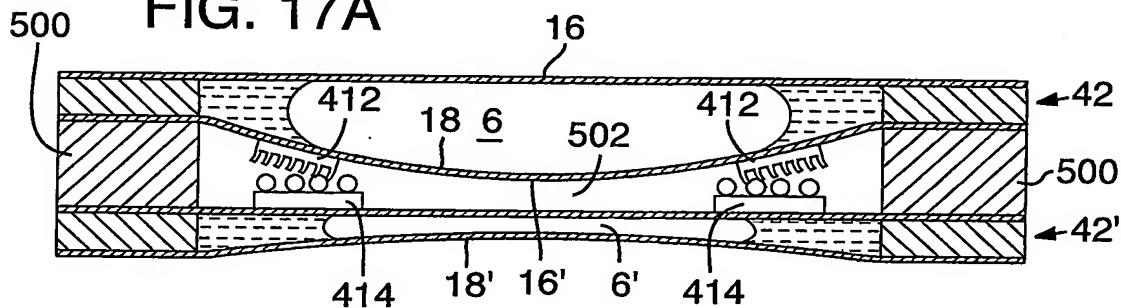
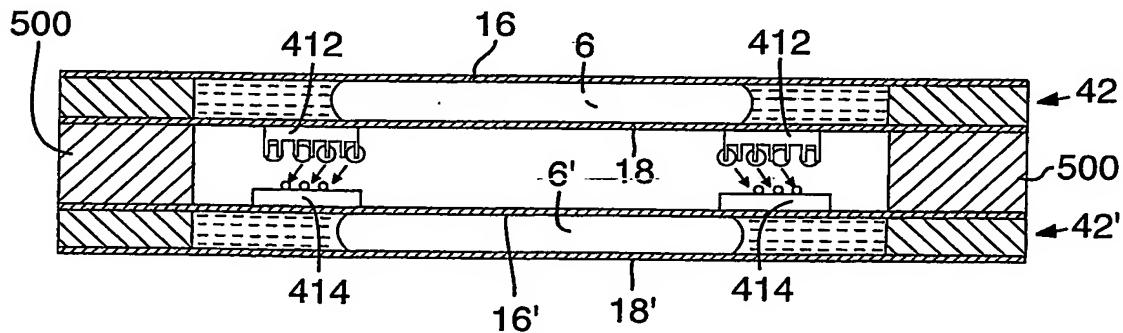


FIG. 17B



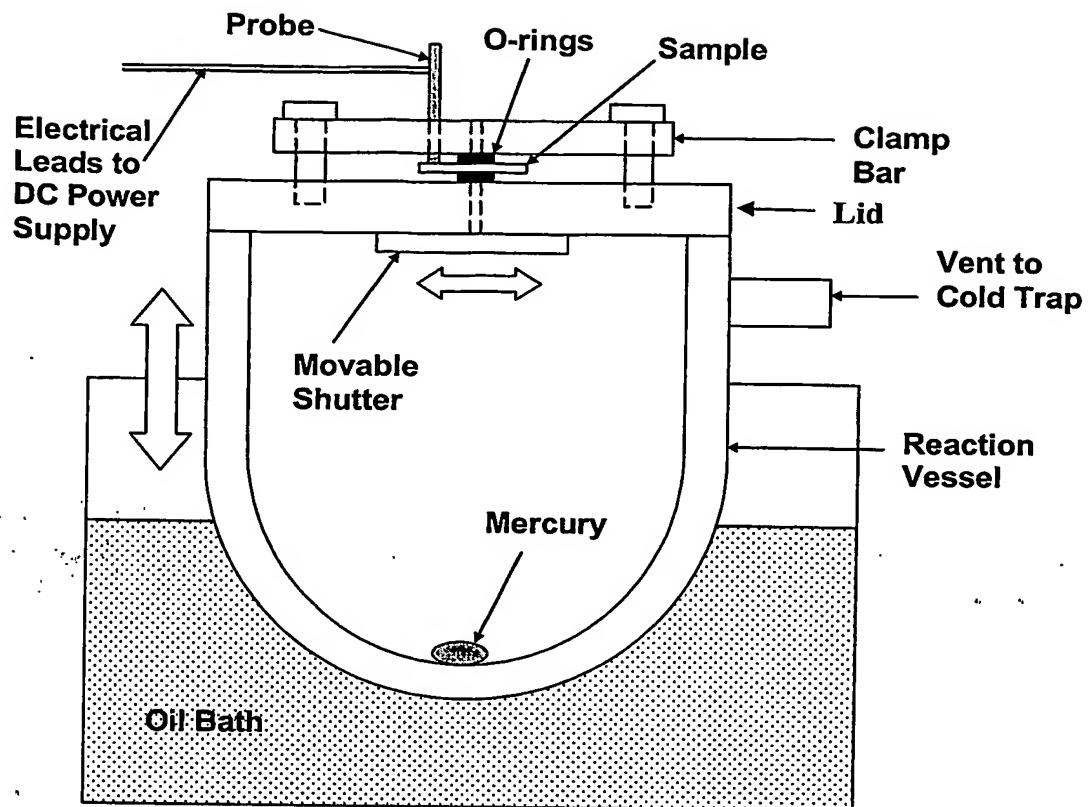


FIG. 18

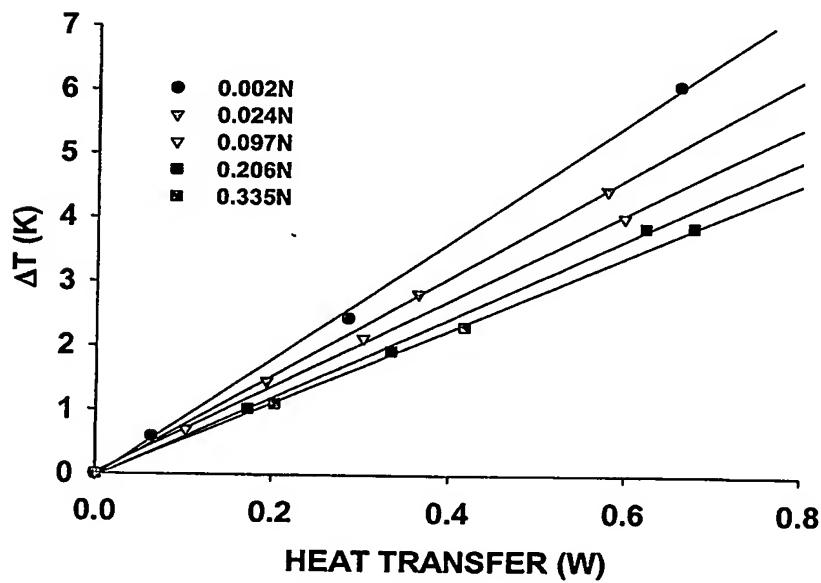


FIG. 19

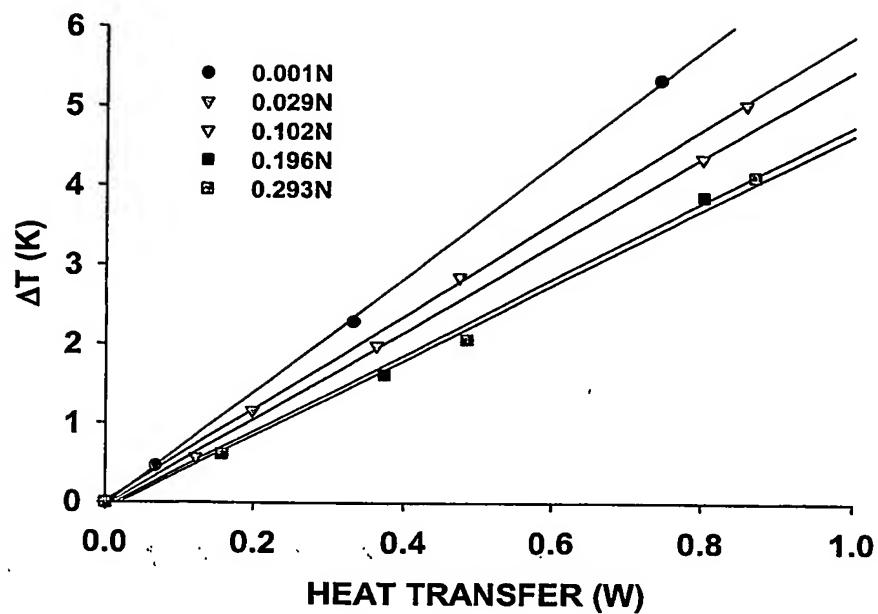


FIG. 20

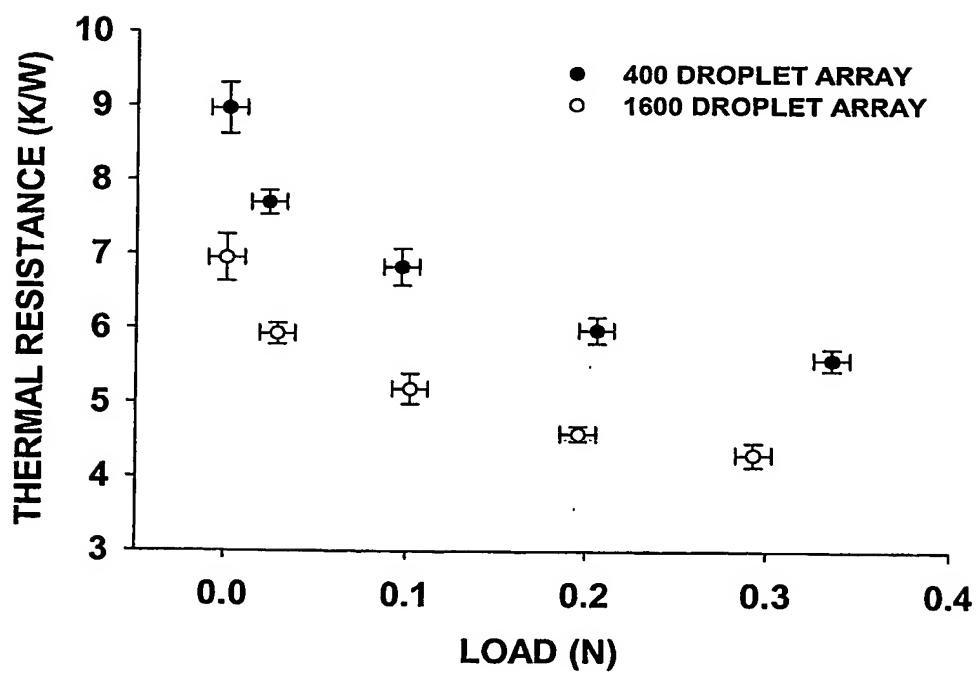


FIG. 21

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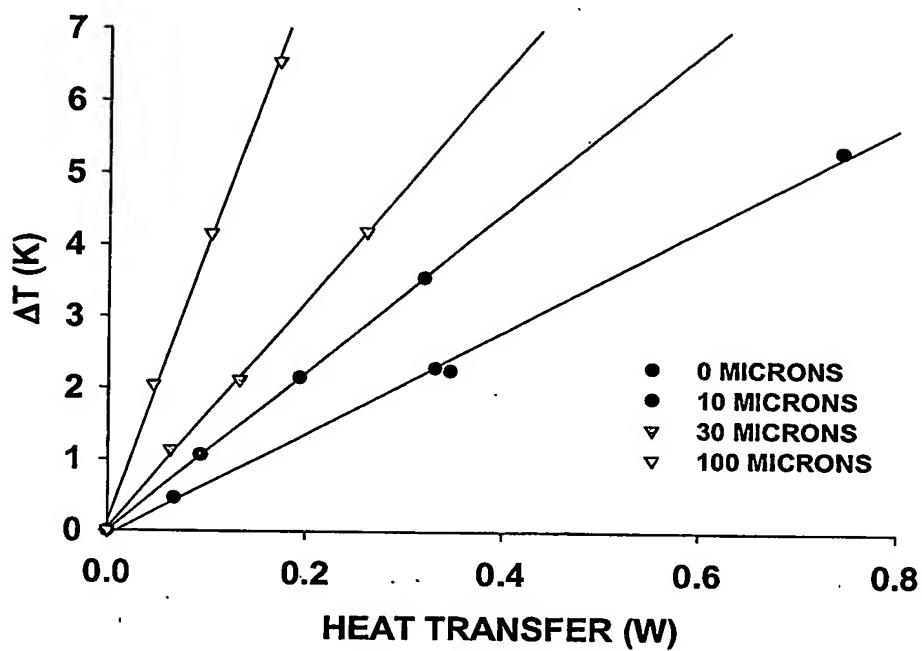


FIG. 22

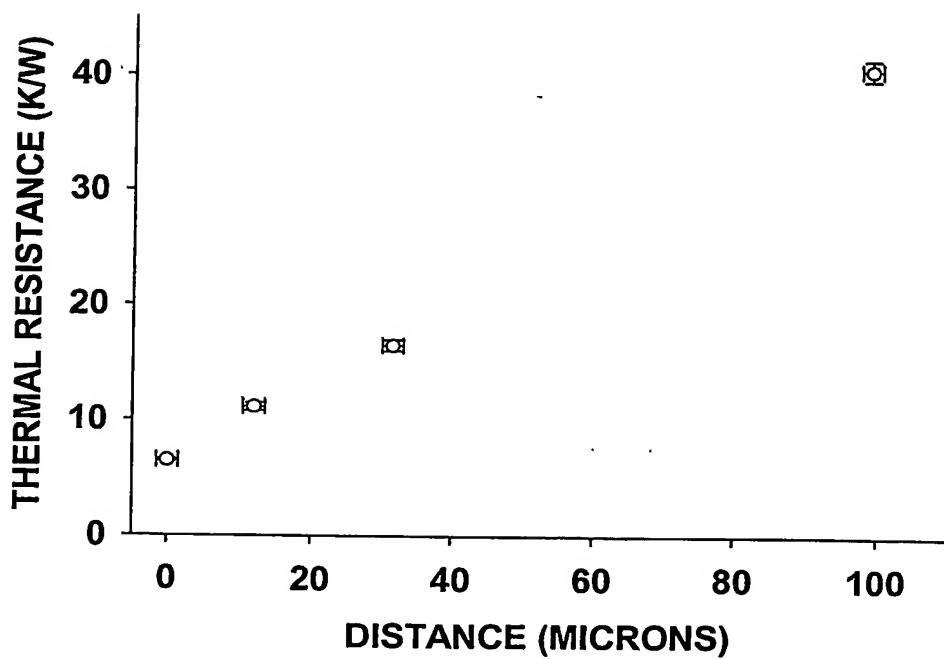


FIG. 23

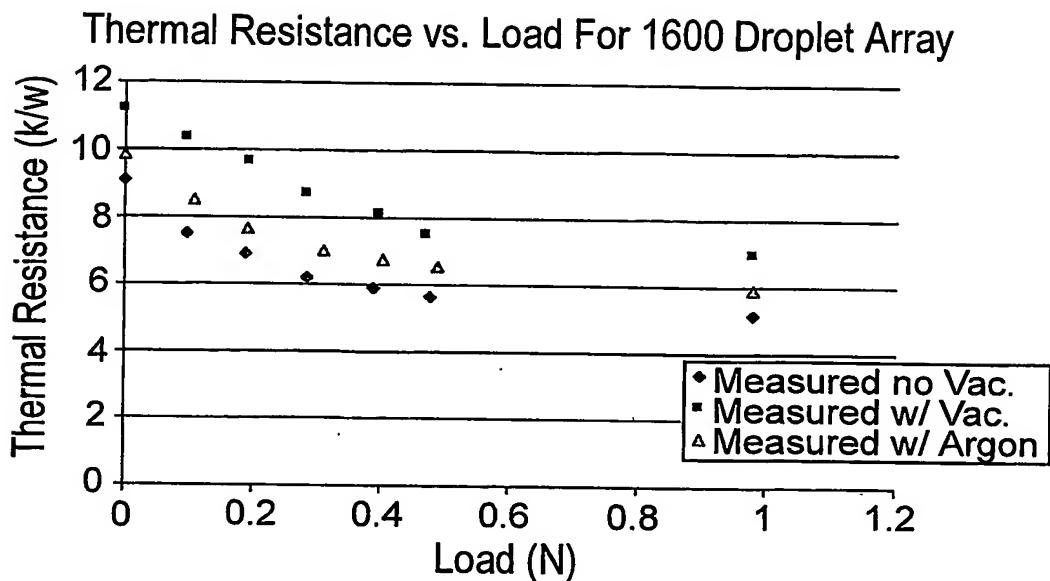


FIG. 24A

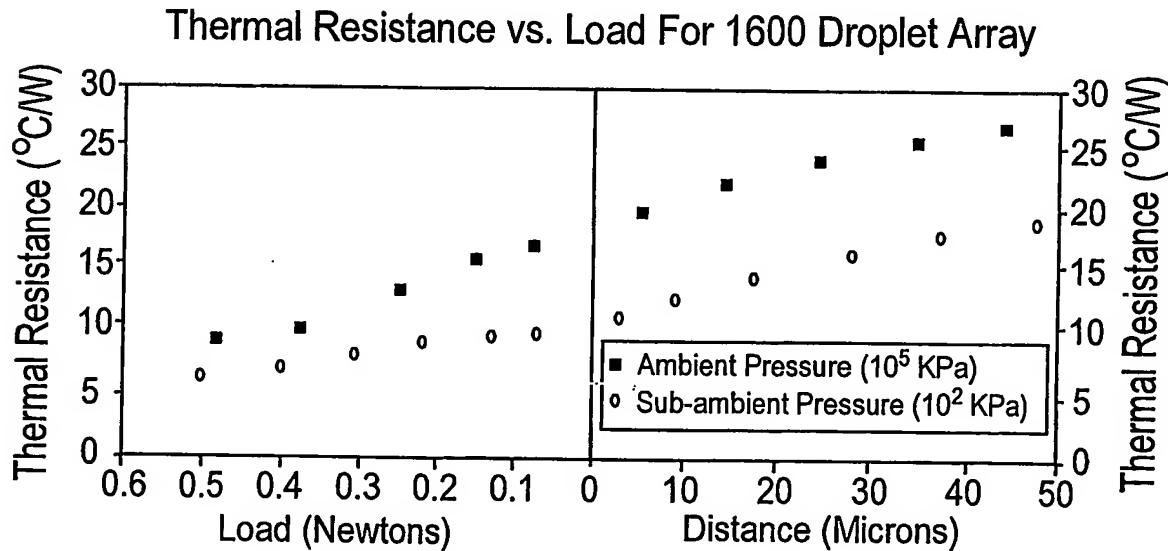


FIG. 24B

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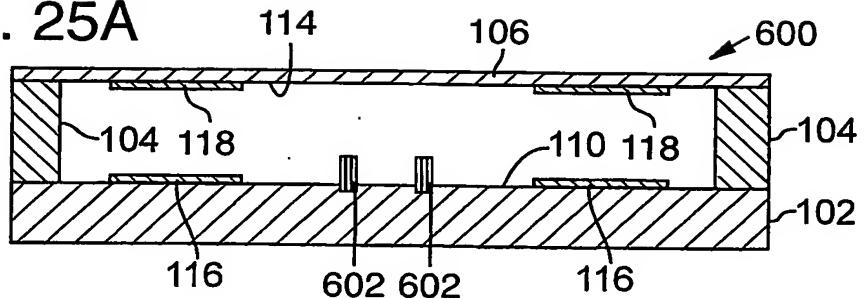
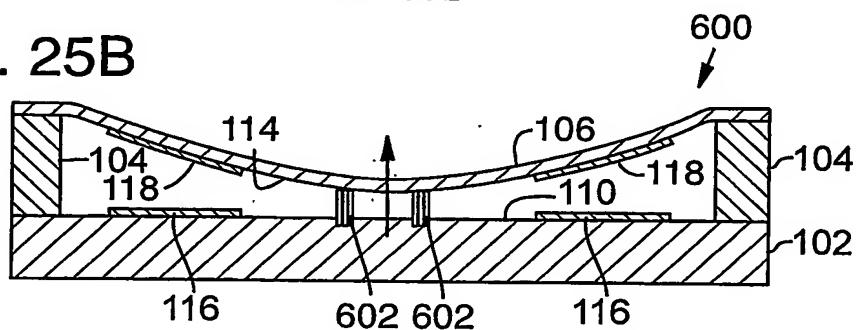
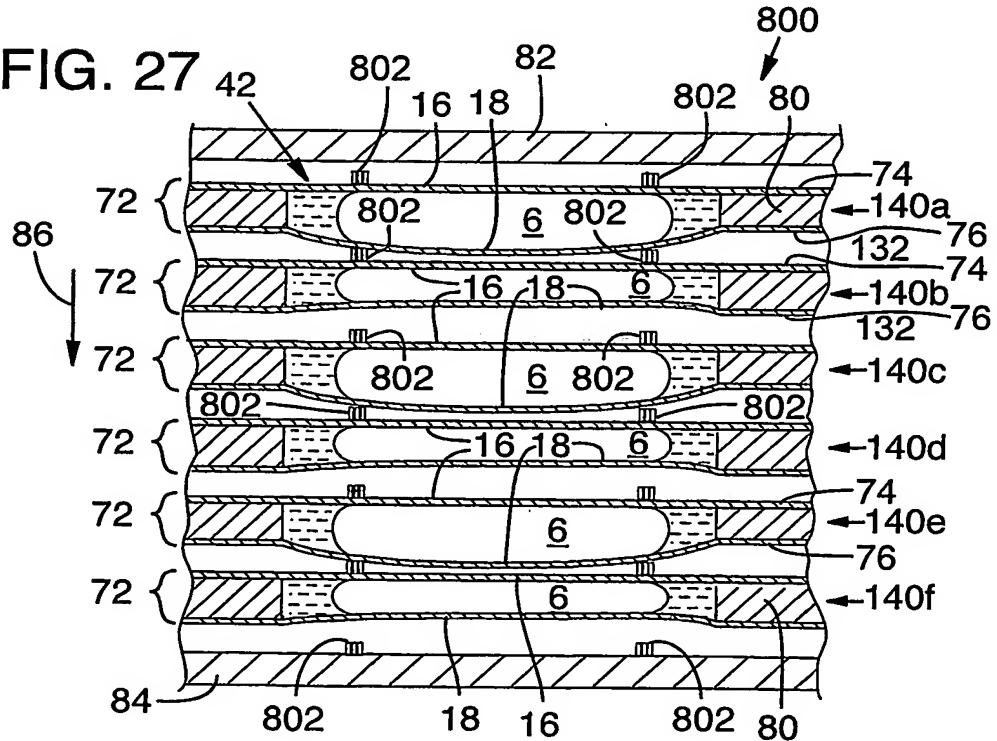
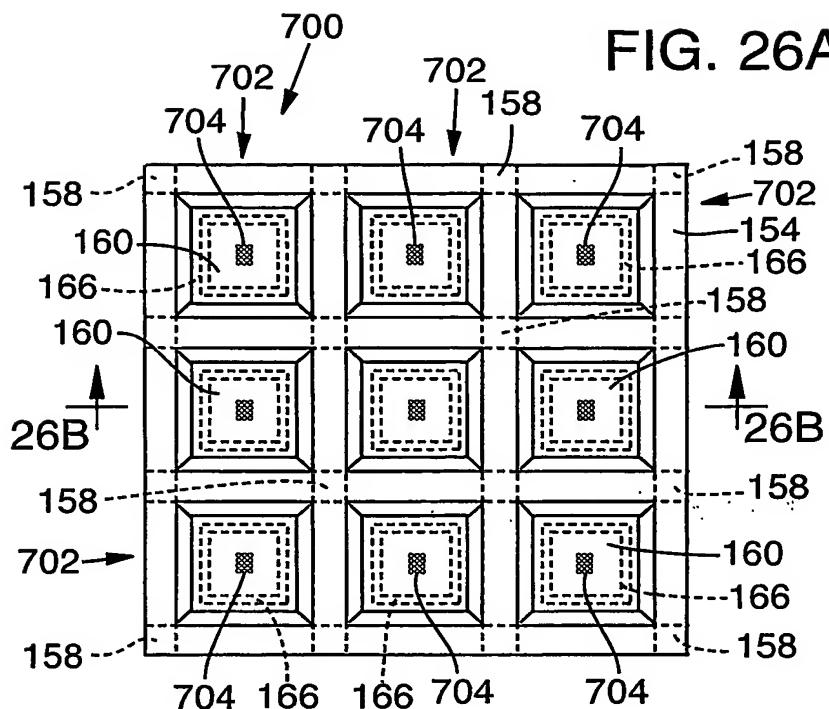
**FIG. 25A****FIG. 25B****FIG. 27**

FIG. 26A



**FIG. 26B**

